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CONTACT INFORMATION

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EDUCATION:

- **Ph.D.** Electronics, Electrical Engineering, May 2018
Faculty of Engineering, Suez Canal University
Thesis title: **“Performance Enhancement of Emerging Memory Technologies Using Memristor–Based Structures”**
- **M. Sc.** Electronics and Communication, Electrical Engineering, October 2012
Faculty of Engineering, Port Said University
Thesis title: **“Analysis and Design of High Performance RTD-CMOS Dynamic Logic Circuits”**
- **B. Sc.** Electrical Engineering, Faculty of Engineering, May 2007
Faculty of Engineering, Port Said, Suez Canal University (Excellent with honor degree).

ACADEMIC EXPERIENCE:

Assistant Professor, May 2018 until now
Electrical Department, Faculty of Engineering, Ismailia, Suez Canal University

Teaching Assistant, January 2008 to May. 2018
Electrical Department, Faculty of Engineering, Ismailia, Suez Canal University

List of courses that I have instructed:

- Engineering Electronics
- Electronic Circuits I
- Electronic Circuits II
- Digital Integrated Circuits
- Analog Integrated Circuits

RESEARCH AND Professional EXPERIENCE

- Technical Reviewer at Microelectronics Journal, Elsevier.
- Technical Reviewer at IEEE International Symposium on Circuits and Systems ISCAS 2020.
- Technical Reviewer at Young Researchers Conference, Suez Canal University, Egypt.

- **Published Papers:**
 1. **S.F. Nafea**, A.A. Dessouki, "An accurate large-signal SPICE model for Resonant Tunneling Diode," 2010 International Conference on Microelectronics (ICM), 2010, pp.507-510.
 2. **S. F. Nafea**, A. A. Dessouki, and S. El-Rabaie, "Memristor Overview up to 2015," Menoufia Journal of Electronic Engineering Research (MJEER), Vol.24, No. 1&2, Jan-July 2015.
 3. **S. F. Nafea**, A. A. Dessouki, S. El-Rabaie, K. El-Barbary, and H. Mostafa, "Read disturbance and temperature variation aware spintronic memristor model," in IEEE Canadian Conference on Electrical and Computer Engineering (CCECE), 2016, pp.1-4.
 4. **S. F. Nafea**, A. A. Dessouki, S. El-Rabaie, Basem E. Elnaghi, Yehea Ismail, and H. Mostafa, "Area-Efficient Read/Write Circuit for Spintronic Memristor Based Memories", 60th IEEE International Midwest Symposium on Circuits and Systems (MWCAS), 2017.
 5. **S. F. Nafea**, A. A. Dessouki, S. El-Rabaie, B. E. Elnaghi, Y. Ismail, and H. Mostafa, "An accurate model of domain-wall-based spintronic memristor," Integration, vol. 65, pp. 149-162, 2019.

RESEARCH INTERESTS

- Electronic devices' Modeling, and Characterization.
- Digital Circuit Design.
- Memory Circuit Design.

OTHER CERTIFICATIONS:

- Competitive research projects
- Effective Teaching
- Utilizing technology in teaching
- Micro-teaching
- Management of research team
- Organizing Conferences
- Ethics of scientific research
- Credit Hour System
- Time and meeting management
- International Scientific Publishing